**ANALOG DEVICES**

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

| | |
|---|---|
| **PCN Title:** | LT8364 Data sheet limit changes. |
| **Publication Date:** | 04-Dec-2019 |
| **Effectivity Date:** | 07-Mar-2020    *(the earliest date that a customer could expect to receive changed material)* |

**Revision Description:**
Initial Release

**Description Of Change:**

Minor changes to the LT8364 product Data sheet.

**Reason For Change:**

The data sheet is being updated to accurately reflect device capabilities.

**Impact of the change (positive or negative) on fit, form, function & reliability:**

The change described above has no impact on fit, form, or functionality of the device.

**Product Identification** *(this section will describe how to identify the changed material)*

The product will be shipped after effectively date will be tested to the new limit.

**Summary of Supporting Information:**

Changes will be reflected on the new product data sheet revision A. See changes on Electrical Characteristics page 3.

**Supporting Documents**

**Attachment 1: Type**: Datasheet Specification Comparison

ADI_PCN_19_0281_Rev_-_LT8364 Data sheet_VIS_PCN.pdf

| For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives. | | | |
|---|---|---|---|
| **Americas:** | **Europe:** | **Japan:** | **Rest of Asia:** |
| PCN_Americas@analog.com | PCN_Europe@analog.com | PCN_Japan@analog.com | PCN_ROA@analog.com |

| Appendix A - Affected ADI Models | | | | |
|---|---|---|---|---|
| Added Parts On This Revision - Product Family / Model Number (12) | | | | |
| LT8364 / LT8364EDE#PBF | LT8364 / LT8364EDE#TRPBF | LT8364 / LT8364EMSE#PBF | LT8364 / LT8364EMSE#TRPBF | LT8364 / LT8364HDE#PBF |
| LT8364 / LT8364HDE#TRPBF | LT8364 / LT8364HMSE#PBF | LT8364 / LT8364HMSE#TRPBF | LT8364 / LT8364IDE#PBF | LT8364 / LT8364IDE#TRPBF |
| LT8364 / LT8364IMSE#PBF | LT8364 / LT8364IMSE#TRPBF | | | |

| Appendix B - Revision History | | | |
|---|---|---|---|
| Rev | Publish Date | Effectivity Date | Rev Description |
| Rev. - | 04-Dec-2019 | 07-Mar-2020 | Initial Release |
| | | | |

# ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25°C$. $V_{IN} = 12V$, EN/UVLO = 12V unless otherwise noted.

| PARAMETER | CONDITIONS | | MIN | TYP | MAX | UNITS |
|---|---|---|---|---|---|---|
| $V_{IN}$ Operating Voltage Range | | ● | 2.8 | | 60 | V |
| $V_{IN}$ Quiescent Current at Shutdown | $V_{EN/UVLO}$ = 0.2V | | | 1 | 2 | µA |
| | | ● | | 1 | 15 | µA |
| | $V_{EN/UVLO}$ = 1.5V | | | 2 | 5 | µA |
| | | ● | | 2 | 25 | µA |
| **$V_{IN}$ Quiescent Current** | | | | | | |
| Sleep Mode (Not Switching) | SYNC = 0V | | | 9 | 15 | µA |
| | | ● | | 9 | 30 | µA |
| Active Mode (Not Switching) | SYNC = 0V or $INTV_{CC}$, BIAS = 0V | | | 1200 | 1600 | µA |
| | | ● | | 1200 | 1850 | µA |
| | SYNC = 0V or $INTV_{CC}$, BIAS = 5V | | | 22 | 40 | µA |
| | | ● | | 22 | 65 | µA |
| BIAS Threshold | Rising, BIAS Can Supply $INTV_{CC}$ | | | 4.4 | 4.65 | V |
| | Falling, BIAS Cannot Supply $INTV_{CC}$ | | | 4 | 4.25 | V |
| $V_{IN}$ Falling Threshold to Supply $INTV_{CC}$ | BIAS = 12V | | | BIAS – 2V | | V |
| BIAS Falling Threshold to Supply $INTV_{CC}$ | $V_{IN}$ = 12V | | | $V_{IN}$ | | V |
| **FBX Regulation** | | | | | | |
| FBX Regulation Voltage | FBX > 0V | ● | 1.568 | 1.6 | ~~1.632~~ 1.636 | V |
| | FBX < 0V | ● | ~~–0.820~~ –0.822 | –0.80 | –0.780 | V |
| FBX Line Regulation | FBX > 0V, 2.8V < $V_{IN}$ < 60V | | | 0.005 | 0.015 | %/V |
| | FBX < 0V, 2.8V < $V_{IN}$ < 60V | | | 0.005 | 0.015 | %/V |
| FBX Pin Current | FBX = 1.6V, –0.8V | ● | –10 | | 10 | nA |
| **Oscillator** | | | | | | |
| Switching Frequency ($f_{OSC}$) | $R_T$ = 165k | ● | ~~273~~ 265 | 300 | 327 | kHz |
| | $R_T$ = 45.3k | ● | ~~0.92~~ 0.90 | 1 | 1.08 | MHz |
| | $R_T$ = 20k | ● | 1.85 | 2 | 2.15 | MHz |
| SSFM Maximum Frequency Deviation | $\Delta f/f_{OSC}$ • 100, $R_T$ = 20k | | 14 | 20 | ~~25~~ 28 | % |
| Minimum On-Time | Burst Mode, $V_{IN}$ = 24V (Note 6) | | | 85 | 110 | ns |
| | Pulse-Skip Mode, $V_{IN}$ = 24V (Note 6) | | | 60 | 85 | ns |
| Minimum Off-Time | | ● | | 50 | 75 | ns |
| SYNC/Mode, Mode Thresholds (Note 5) | High (Rising) | ● | | 1.3 | 1.7 | V |
| | Low (Falling) | ● | 0.14 | 0.2 | | V |
| SYNC/Mode, Clock Thresholds (Note 5) | Rising | ● | | 1.3 | 1.7 | V |
| | Falling | ● | 0.4 | 0.8 | | V |
| $f_{SYNC}/f_{OSC}$ Allowed Ratio | $R_T$ = 20k | | 0.95 | 1 | 1.25 | kHz/kHz |
| SYNC Pin Current | SYNC = 2V | | | 10 | 25 | µA |
| | SYNC = 0V, Current Out of Pin | | | 10 | 25 | µA |
| **Switch** | | | | | | |
| Maximum Switch Current Limit Threshold | | ● | 4 | 5 | 6.4 | A |
| Switch Overcurrent Threshold | Discharges SS Pin | | | 7.5 | | A |
| Switch $R_{DS(ON)}$ | $I_{SW}$ = 0.5A | | | 100 | | mΩ |
| Switch Leakage Current | $V_{SW}$ = 60V | | | 0.1 | 1 | µA |